REMARKS

Summary of the Office Action

Claims 1, 4-8 and 12-14 stand rejected under 35 U.S.C. §102(b) as being anticipated by

Kim et al. (US 6,100,953).

Claims 2 and 9-11 stand rejected under 35 U.S.C. §103(a) as being unpatentable over

Kim et al. in view of Woo et al. (US 6,067,140).

Claims 15-17 stand rejected under 35 U.S.C. § 112, first paragraph.

Applicants wish to thank the Examiner for the remarks made in the Advisory Action

dated January 26, 2004. Specifically, Applicants appreciate the indication that the Examiner

concurs with Applicants' arguments concerning the alleged new matter of slit patterns within a

unit pixel region.

Summary of the Advisory Action

In the Advisory Action dated January 26, 2004, the Examiner indicates that "the

applicant goes into length discussion about the slit patterns within a unit pixel region for which

the Examiner fully concurs with the applicants' arguments' (emphasis added). Thus, Applicants

respectfully submit that the present Response is being filed to further clarify arguments made in

the prior Response submitted on December 23, 2003. Specifically, Applicants provide further

clarification regarding support under 35 U.S.C. § 112, first paragraph, for the "first electrode and

the light-shielding layer within a same unit pixel region," as recited by claims 15, 16, and 17.

Summary of the Response to the Office Action

Claims 2, 5-7, and 9-17 have been amended, and claims 1, 3, 4, and 8 have been

canceled. Accordingly, claims 2, 5-7, and 9-17 are currently pending.

1-WA/2136776.1

All Claims Comply With 35 U.S.C. § 112

Claims 15-17 stand rejected under 35 U.S.C. §112, first paragraph, as allegedly containing subject matter which was not described in the specification in such a way as to reasonably convey to one skilled in the relevant art that the inventors, at the time the application was filed had possession of the claimed invention. In particular, the Final Office Action asserts that the feature of "both the first electrode and the light-shielding layer are within a same unit pixel region," as set forth in claims 15-17 is new matter. See page 3, lines 1-4 of the Final Office Action. Applicants respectfully traverse the rejection for at least the following reasons.

Applicants respectfully submit that one skilled in the relevant art would recognize that gate and data lines, which typically are arranged substantially perpendicular to each other and crossing each other, define a plurality of pixel regions. Applicants further respectfully submit that one skilled in the relevant art would recognize that a TFT and a pixel electrode are within each of the pixel regions. For instance, the primarily cited reference, <u>Kim et al.</u>, discloses that

"[i]n the conventional LCD, a plurality of gate bus lines arranged in a first direction on a first substrate and a plurality of data bus lines arranged in a second direction on the first substrate divide the first substrate into a plurality of pixel regions. A thin film transistor (TFT) applies an image signal delivered from the data bus line to a pixel electrode 13 on a passivation layer. The TFT is formed on each pixel region." Column 1, lines 26-32 of Kim et al."

In addition, Applicants respectfully submit that, as disclosed at paragraphs [0038], [0044], and [0047] and shown in FIGs. 3, 5, and 6 of the originally-filed specification, a first transparent electrode 22 having slit patterns 21 is formed on a first substrate and a black matrix 25 is formed below the slit patterns 21. Accordingly, Applicants respectfully assert that, especially in light of the disclosure of <u>Kim et al.</u>, the originally-filed specification would

reasonably convey to one skilled in the relevant art that gate and data lines define pixel regions,

that each pixel region has one TFT and one pixel electrode, and that the transparent electrode,

including the slit patterns and black matrix of claims 15, 16, and 17, are formed within a "same

unit pixel region." Thus, Applicants respectfully submit that the originally-filed specification

would reasonably convey to one skilled in the relevant art that the present invention includes a

first electrode having a plurality of slit patterns and at least one light-shielding layer, wherein

"both the first electrode and the light-shielding layer are within a same unit pixel region," as

recited by claims 15, 16, and 17.

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In view of the above, Applicants respectfully submit that the originally-filed specification

complies with the requirements under 35 U.S.C. §112, first paragraph, and that claims 15-17

contain subject matter which was described in the specification in such a way as to reasonably

convey to one skilled in the relevant art that the inventors, at the time the application was filed,

had possession of the claimed invention. Accordingly, Applicants respectfully request that the

rejection of claims 15-17 under 35 U.S.C. § 112, first paragraph, be withdrawn.

All Claim Define Allowable Subject Matter

Claims 1, 4-8 and 12-14 stand rejected under 35 U.S.C. §102(b) as being anticipated by

Kim et al. (US 6,100,953), and claims 2 and 9-11 stand rejected under 35 U.S.C. §103(a) as

being unpatentable over Kim et al. in view of Woo et al. (US 6,067,140).

Applicants respectfully submit that since none of claims 15-17 stand rejected under any

prior art and in accordance with MPEP § 2163.06(I), the Examiner should have considered the

alleged new subject matter, claims 2, 5-7, and 9-17 are in clear condition for allowance and are

allowable over the prior art.

1-WA/2136776.1

ATTORNEY DOCKET NO.: 041501-5452

Application No.: 09/955,979

Page 9

Conclusion

In view of the foregoing, withdrawal of the rejections and allowance of the pending

claims are earnestly solicited. Should there remain any questions or comments regarding this

response or the application in general, the Examiner is urged to contact the undersigned at the

number listed below.

If there are any other fees due in connection with the filing of this response, please charge

the fees to our Deposit Account No. 50-0310. If a fee is required for an extension of time under

37 C.F.R. § 1.136 not accounted for above, such extension is requested and the fee should also

be charged to our Deposit Account.

Respectfully submitted,

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Dated: February 20, 2004

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